



41

PATENT

2811 / 4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/881,005
Filed: June 14, 2001
Inventor: Michio Horiuchi
Title: **Multi-Layered Semiconductor
Device and Method for
Producing the Same**
Attny Doc. No.: 149-01
Art Unit: 2811
Examiner: Douglas W. Owens

Certificate of Class Mail

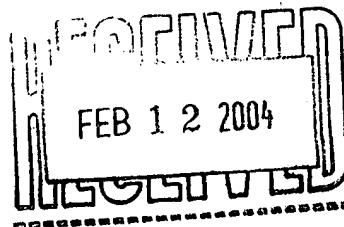
I hereby certify that this correspondence is being deposited with the United States Post Office as "First Class Mail" under 37 C.F.R. §1.8 on the date indicated below and is prepaid in an envelope addressed to: Mail Stop Fee Amendment Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on: January 30, 2004


Dara Salgado

COVER LETTER WITH CERTIFICATE OF FIRST CLASS MAIL

Mail Stop Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:



RECEIVED
FEB 11 2004
TECHNOLOGY CENTER 2800

Enclosed and attached hereto are the following:

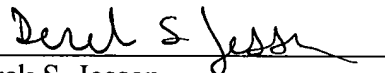
- 1) Cover Letter with Certificate of Mailing;
- 2) Amendment;
- 3) Petition for One Month Extension of Time;
- 4) Check in the amount of \$110.00; and
- 5) Postcard to be date-stamped and returned.

No additional fees are believed to be required. In the event that an additional fee is

required with respect to this communication, the Commissioner is hereby authorized to charge any additional fees, or credit any overpayment, to Paul & Paul Deposit Account No.16-0750.

Respectfully submitted,

Date: January 30, 2004


Derek S. Jessen
Reg. No. 48,213
Paul & Paul
2900 Two Thousand Market Street
Philadelphia, PA 19103
(215) 568-4900

Order No.: 2044